L Number	Hits	Search Text	DB	Time stamp
2	2066	(710/8-14).ccls.	USPAT;	2004/07/15 07:57
			US-PGPUB;	
			EPO; JPO;	
	5.00	(714/05 46)	DERWENT	2004/07/15 07:58
3	6426	(714/25-46).ccls.	USPAT; US-PGPUB;	2004/07/15 07:58
			EPO; JPO;	
			DERWENT	
1	290	703/21.ccls.	USPAT;	2004/07/15 09:42
			US-PGPUB;	
			EPO; JPO;	
	0.605	703/21.ccls. or ((710/8-14).ccls.) or	DERWENT USPAT;	2004/07/15 09:42
4	8695	((714/25-46).ccls.)	US-PGPUB;	2004/07/13 09.42
		((/14/25 40).0015.)	EPO; JPO;	
			DERWENT	
5	9947	DUT or (device adj under adj test)	USPAT;	2004/07/15 09:45
			US-PGPUB;	
			EPO; JPO;	
	201001	1 1 - + 6 4	DERWENT USPAT;	2004/07/15 09:46
6	281891	simulat\$4	US-PGPUB;	2004/07/13 07.40
			EPO; JPO;	
			DERWENT	
7	25984	debug\$4	USPAT;	2004/07/15 09:47
			US-PGPUB;	
			EPO; JPO; DERWENT	
8	88384	 verificat\$4	USPAT;	2004/07/15 09:47
0	00304	Verificativa	US-PGPUB;	2001,01,10 0311
İ			EPO; JPO;	
			DERWENT	
9	37347	emulat\$4	USPAT;	2004/07/15 09:47
			US-PGPUB; EPO; JPO;	
			DERWENT	
10	1053307	peripheral	USPAT;	2004/07/15 09:48
			US-PGPUB;	
			EPO; JPO;	
	070	(700/01] (/710/0 14)1-)	DERWENT	2004/07/15 09:49
11	273	(703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device	USPAT; US-PGPUB;	2004/07/13 09:49
		adj under adj test))	EPO; JPO;	
		adj andst adj soodii	DERWENT	
12	273	(DUT or (device adj under adj test)) and	USPAT;	2004/07/15 09:49
		((703/21.ccls. or ((710/8-14).ccls.) or	US-PGPUB;	
		((714/25-46).ccls.)) and (DUT or (device	EPO; JPO; DERWENT	
13	94	adj under adj test))) simulat\$4 and ((703/21.ccls. or	USPAT;	2004/07/15 09:49
13]	((710/8-14).ccls.) or ((714/25-46).ccls.))	US-PGPUB;	
		and (DUT or (device adj under adj test)))	EPO; JPO;	
			DERWENT	0004/05/15 00 50
14	43	,	USPAT;	2004/07/15 09:50
	ļ	or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device	US-PGPUB; EPO; JPO;	
		adj under adj test))))	DERWENT	
15	21		USPAT;	2004/07/15 09:50
		and ((703/21.ccls. or ((710/8-14).ccls.)	US-PGPUB;	
		or ((714/25-46).ccls.)) and (DUT or	EPO; JPO;	
16		(device adj under adj test))))) emulat\$4 and (verificat\$4 and (debug\$4 and	DERWENT USPAT;	2004/07/15 09:51
16	9	(simulat\$4 and (verificat\$4 and (debug\$4 and (simulat\$4 and ((703/21.ccls. or	US-PGPUB;	2004/01/13 03:31
		((710/8-14).ccls.) or ((714/25-46).ccls.))	EPO; JPO;	
		and (DUT or (device adj under adj	DERWENT	
	1	test))))))		
17	3		USPAT;	2004/07/15 09:51
	1	and (debug\$4 and (simulat\$4 and	US-PGPUB;	
		((703/21.ccls. or ((710/8-14).ccls.) or ((714/25-46).ccls.)) and (DUT or (device	EPO; JPO; DERWENT	
		adj under adj test))))))		
L		uu		

L Number	Hits	Search Text	DB	Time stamp
1	0	simulat\$4 and (microprocessor or micro near processor or processor) same clock and simulat\$5 and debug\$6 and perl and	USPAT; US-PGPUB;	2004/07/15 17:34
		BIOS and DOS and format\$3 near3 disk	EPO; JPO; DERWENT;	
2	0	simulat\$4 same (microprocessor or micro near processor or	IBM_TDB USPAT;	2004/07/15 17:34
3		processor) and clock and debug\$6 and perl and BIOS and DOS and format\$3 near3 disk	US-PGPUB; EPO; JPO; DERWENT;	
	0	simulat\$4 same (microprocessor or micro near processor or	IBM_TDB USPAT;	2004/07/15 17:34
3	0	processor) and clock and debug\$6 and perl and BIOS and DOS and format\$3 near3 disk	US-PGPUB; EPO; JPO; DERWENT;	250 1/0//13 17:31
4	0	simulat\$4 same (microprocessor or micro near processor or processor) and debug\$6 and BIOS and DOS and format\$3 near3	IBM_TDB USPAT; US-PGPUB;	2004/07/15 17:35
		disk	EPO; JPO; DERWENT; IBM_TDB	
5	0	simulat\$4 same (microprocessor or micro near processor or processor) and debug\$6 and BIOS and format\$3 near3 disk	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 17:35
			DERWENT; IBM_TDB	
-	85	simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 17:27
		<u> </u>	DERWENT; IBM_TDB	
-	29	(simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:24
			DERWENT; IBM_TDB	
-	28	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) or ((simulat\$4 and (microprocessor	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:04
		or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8)) and verilog	DERWENT; IBM_TDB	
-	2	"US 20020107678 A1"	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:05
			DERWENT; IBM_TDB	
-	14	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:49
		processor near3 clock	DERWENT; IBM_TDB	
-	3	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:52
		(register near transfer near level or rtl) near2 model and gate near2 model	DERWENT; IBM_TDB	
-	13	((simulat\$4 and (microprocessor or micro near processor or processor) near5 clock and (peripheral near2 device or device) near5 simulat\$5 and debug\$6) and virtual and verif\$8) and	USPAT; US-PGPUB; EPO; JPO;	2004/07/15 13:53
		(register near transfer near level or rtl) near2 model	DERWENT; IBM_TDB	

_	15	1 //	USPAT;	2004/07/15 13:53
		processor) near5 clock and (peripheral near2 device or device)	US-PGPUB;	
		near5 simulat\$5 and debug\$6) and virtual and verif\$8) and	EPO; JPO;	
		(register near transfer near level or rtl)	DERWENT;	
			IBM_TDB	
-	1	simulat\$4 and (microprocessor or micro near processor or	USPAT;	2004/07/15 16:48
	_	processor) near5 clock and (peripheral near2 device or device)	US-PGPUB;	
		near5 simulat\$5 and debug\$6 and motif near2 library	EPO; JPO;	
		,	DERWENT:	
			IBM TDB	